1. Embedded Systems definition, specification, and languages.

An embedded system is nearly any computing system other than a desktop computer.

An embedded system is a dedicated system which performs the desired function upon power up, repeatedly.

Embedded systems are found in a variety of common electronic devices such as consumer electronics ex. Cell phones, pagers, digital cameras, VCD players, portable Video games, calculators, etc.

**Structure-oriented models describe the system's physical modules and the interconnections between them**.

The major areas of the design process are

* + 1. Ensuring a sound software and hardware specification.
    2. Formulating the architecture for the system to be designed.
    3. Partitioning the h/w and s/w.
    4. Providing an iterative approach to the design of h/w and s/w.

The important steps in developing an embedded system are

1. Requirement definition.
2. System specification.
3. Functional design
4. Architectural design
5. Prototyping.

The system design specification

The System Design Specification (SDS) is a complete document that contains all of the information needed to develop the system.

Systems design is the process of defining the architecture, components, modules, interfaces, and data for a system to satisfy specified requirements.

Systems design could be seen as the application of systems theory to product development. There is some overlap with the disciplines of systems analysis, systems architecture and systems engineering.

System design specification serves as a bridges between the customers and designers.

It must specify the system‘s public interface from inside the system.

Five areas should be considered are:

1. Geographical constraints.
2. Characterization of and constraints on interface signals.
3. User interface requirements Temporal constraints.
4. Electrical infrastructure consideration

V. Safety and reliability

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# Best Embedded Systems Programming Languages

Embedded Systems Programming is an exclusive industry and only a few programming languages are allowed entry because there are specific requirements such as low usage of resources as well as low-level system access.

**1.**[C](https://www.geeksforgeeks.org/c-programming-language/)

* The loose data typing policy of C makes it quite suitable.
* It is simple to port the embedded programs from one device to the next as compared to other languages.
* The widespread community in C provides vast support for Embedded Systems Programming.

**2.**[C++](https://www.geeksforgeeks.org/c-plus-plus/)

* C++ is less popular than C in regards to embedded systems but the addition of object-oriented programming.
* It also has low-level access to memory like C that makes it quite suitable.
* C++ is more secure than C because of its use of string literals, enumeration constants, [templates](https://www.geeksforgeeks.org/templates-cpp/) etc.
* The object oriented nature of C++ is also quite useful for complex embedded systems programming.

**3.**Java

* Java can be used to write extensible, portable, and downloadable embedded systems applications.
* There are many DevOps tools and libraries in Java that make it suitable for Embedded Systems Programming.
* The Java Virtual Machine ensures that embedded systems programmed in Java are portable and can be used for different [IoT](https://www.geeksforgeeks.org/introduction-to-internet-of-things-iot-set-1/) platforms.

**4.**[Python](https://www.geeksforgeeks.org/python-programming-language/)

* Python is a popular language and known for its writability, concise, readable coding style, and error deduction.
* Python is much handier in the case of complicated embedded systems such as those using neural networks.
* Real time embedded systems use Python quite often. *MicroPython*is a good example of a lean and efficient implementation for this.

**5. Rust**

* Rust allows memory management using both dynamic and static methods using various tools.
* Rust can be used to program a wide range of embedded systems from small micro controllers to large multifaceted systems.
* There is a large community support in Rust for embedded systems programming.

**6. Ada**

* Ada is useful for embedded systems programming because of strong typing, run-time checking, parallel processing, exception handling, generics etc.
* Ada packages can be compiled separately as it was created for the development of large software systems.

**7. Lua**

* The base language in Lua is quite light as it has various meta-features that can be extended as required.
* Lua can implement object oriented programming using first-class functions and tables.
* Lua is cross-platform and it supports a C API that can be embedded into applications.

**8. B#**

* B# is well suited for small scale embedded systems programming because of its tiny core and small memory constraints.
* B# is type safe and does not allow any pointer manipulation which is an asset in embedded systems programming.
* The code written in B# is directly mapped to a tight instruction set. This reduces the runtime in low resource embedded devices.

**9. Verilog**

* Verilog can be used to design custom hardware that is required by the embedded system.
* Verilog has a module hierarchy wherein the modules can communicate with each other using the input, output, and bidirectional ports.
* The modules contain concurrent and sequential statement blocks. These blocks are executed concurrently and so Verilog is a dataflow language.

**10. Embedded C++**

* Embedded C++ aims to minimize the code size and maximize the execution efficiency in regards to embedded systems programming.
* A standard C++ compiler can be used to compile the embedded systems programming done in Embedded C++.
* Some of the specific compilers for Embedded C++ are provided by Freescale Semiconductor, Green Hills Software etc.

1. Embedded system models at different abstraction levels.

**Levels of abstraction**  
ABSTRATION:

* A level of abstraction is usually understood in terms of the language of design. Often, the language defines the level, but that doesn't have to be the case.
* A level of abstraction is determined by the objects that can be manipulated and the operations that can be performed on them.
* In programming terms, the objects are data types and the operations are the operators that can be used in expressions and control constructs.

Diagram

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**Interpretation**  
Interpretation is a technique for dynamically realizing the semantics of a language.

* A good example of a level of abstraction being implemented by interpretation is a micro-coded processor.
* The instructions visible to the programmer are implemented by a lower level processor which interprets the programmer-visible instructions.
* You can also make the argument that the use of a low-level VLIW processor to implement an algorithm is an example of interpretation. There aren't many other examples of the use of interpretation in hardware design.
* **Translation**  
  Translation is the most easily recognized form of abstraction implementation. Synopsys' Design Compiler translates RTL Verilog into a netlist, a process called logic synthesis.
* There has never been a widely accepted definition of the register-transfer level. The traditional definition is that RTL is whatever Design Compiler accepts.
* A better definition is that RTL is a representation where the activity in each clock cycle is explicitly defined.
* Behavioral synthesis was an easily identified analog of logic synthesis. It translated language at a behavioral level to RTL (or directly to gates).

A drawing on a white board

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* **Extension**  
  Extension is not as widely recognized in hardware design as translation and interpretation, but is potentially the most useful.
* Extension is accomplished by defining new data types and operations in the same language that is used in the next lower level.
* Object oriented languages make creating new levels of abstraction in a single language particularly easy, and in fact, that is the primary virtue of C++.
* SystemC is nothing more than a hardware level of abstraction implemented by extension in C++.
* More important is that using C++, additional data types and operations can be defined to create higher levels than the base SystemC level.

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1. **REAL-TIME OPERATING SYSTEM (RTOS)**

* RTOS is a multitasking operating system intended for real time applications.
* In the OS, there is a module called the scheduler, which schedules different tasks and determines when a process will execute on the processor.Multi-tasking is achieved in this way.
* The scheduler in RTOS is designed to provide a predictable execution pattern. In an embedded system, a certain event must be carried across strictly defined time.
* To meet real time requirements, the behavior of scheduler must be predictable.
* This type of OS which have a scheduler with predictable execution pattern is called real time OS (RTOS). The features of an RTOS are:

1. Context switching latency should be short.
2. Interrupt latency should be short.
3. Interrupt dispatch latency should be short.
4. Reliable and time bound inter process mechanism.
5. Support kernel pre-emption.

### Types of real-time systems based on timing constraints:

1. **Hard real-time system:**This type of system can never miss its deadline. Missing the deadline may have disastrous consequences. The usefulness of results produced by a hard real-time system decreases abruptly and may become negative if tardiness increases. Tardiness means how late a real-time system completes its task with respect to its deadline. Example: Flight controller system.
2. **Soft real-time system:** This type of system can miss its deadline occasionally with some acceptably low probability. Missing the deadline have no disastrous consequences. The usefulness of results produced by a soft real-time system decreases gradually with an increase in tardiness. Example: Telephone switches.

Real-time systems are used in:

* Airlines reservation system.
* Air traffic control system.
* Systems that provide immediate updating.
* Used in any system that provides up to date and minute information on stock prices.
* Defense application systems like RADAR.
* Networked Multimedia Systems
* Command Control Systems
* Internet Telephony
* Anti-lock Brake Systems
* Heart Pacemaker

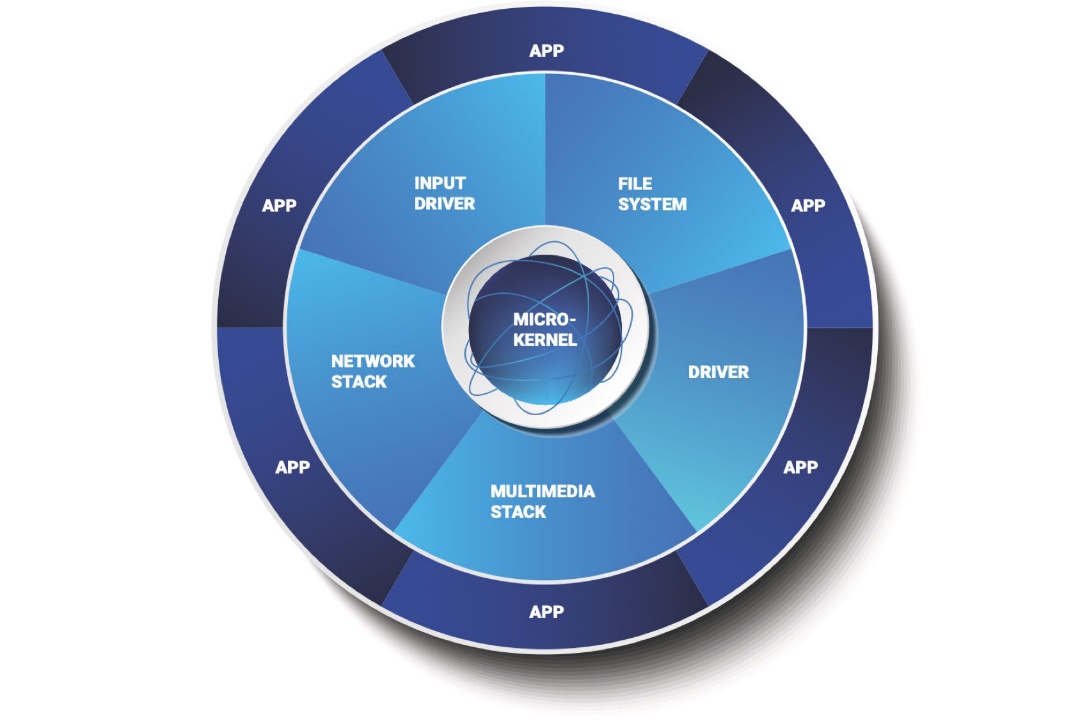
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**Microkernel RTOS**

A microkernel RTOS is structured with a tiny kernel that provides minimal services. The microkernel works with a team of optional cooperating processes that run outside the kernel space (in the user space), which provides higher-level OS functionality.

**Microkernel OS Architecture**



The microkernel design and modular architecture enable BlackBerry QNX customers to create highly optimized and reliable systems with low total cost of ownership. With the QNX Neutrino RTOS, embedded systems designers can create compelling, safe and secure devices

1. **Elaborate the Design, analysis techniques for decentralized computer architectures.**

Cyber-Physical Production Systems (CPPSs) are complex [manufacturing systems](https://www.sciencedirect.com/topics/engineering/gas-fuel-manufacture) which aim to integrate and synchronize machine world and manufacturing facility to the cyber computational space. However, having intensive [interconnectivity](https://www.sciencedirect.com/topics/engineering/interconnectivity) and a computational platform is crucial for real-world implementation of CPPSs.

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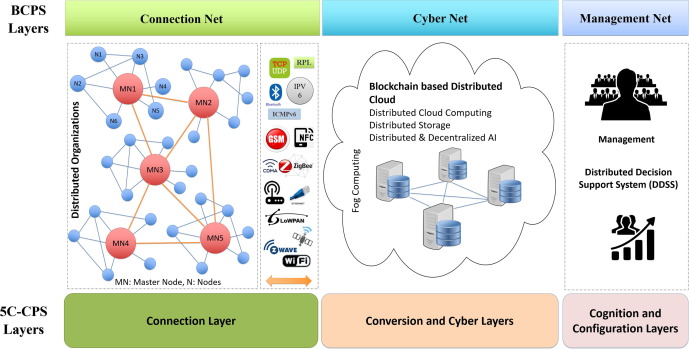
**Decentralized System Architecture**

The system architecture for Cyber Physical Production Systems (CPPS) consists of three distinct phases:

1) ***Automatic Production Plan Generation***: We automatically generate the production plan based on a system description and formalized specifications.

2) ***Production Plan Validation***: The timing, safety, and functional correctness of the generated production plan are validated.

3) ***Decentralized Two-Stage Consensus***: The decision-making process is built on a two-stage consensus algorithm, featuring a majority agreement on the safety and optimality and a unanimous agreement of all executing devices on the feasibility and authenticity of the plan. The hardware design of the architecture is visualized in Figure 2. We consider a mesh network of heterogeneous devices with varying functionalities and computational capabilities. The generation of the production plans is outsourced to cloud devices, whereas the functional validation can be performed on the computationally stronger devices in the mesh network as well.



1. **Explain the cyber physical system hardware platform.**

The [European project](https://cordis.europa.eu/project/rcn/194141_en.html), a three-year program which began in February 2015 and received 2.88 million euros (about $3.5 million) in funding from the European Commission under the Horizon 2020 program, addresses the lack of simulation tools and models for full system design and analysis. This is mainly because most existing simulation tools for complex CPS only efficiently handle parts of a system while mainly focusing on performance.

Diagram

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They also require extreme amounts of processing resources and computation time to accurately simulate the CPS nodes’ processing. Faster approaches are available, however as they function at high levels of abstraction, they cannot provide the accuracy required to model the exact behavior of the system under design to guarantee that it meets the requirements in terms of performance and/or energy consumption.

[**COSSIM**](http://www.cossim.org/)**framework:**

The new open source [COSSIM](http://www.cossim.org/) framework, as it is called, developed in a R&D cooperation between STMicroelectronics with Politecnico di Milano, Telecommunication Systems Institute and Synelixis, was successfully evaluated on an advanced client-server visual search use case that detects instances of objects. The evaluation included co-simulation with an experimental embedded system made by ST Microelectronics.

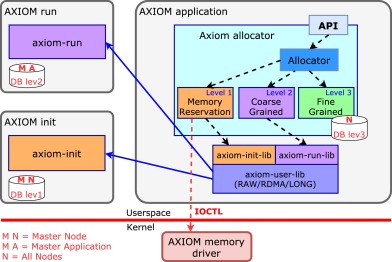
Diagram

Description automatically generated**COSSIM framework**

**AXIOM:**

The AXIOM project (Agile, eXtensible, fast I/O Module) aims at developing a hardware-software platform for CPS such that i) it can use an easy parallel programming model and ii) it can easily scale-up the performance by adding multiple boards (e.g., 1 to 10 boards can run in parallel). AXIOM supports task-based programming model based on OmpSs and leverage a high-speed, inexpensive communication interface called AXIOM-Link. Another key aspect is that the board provides programmable logic (FPGA) to accelerate portions of an application. We are using smart video surveillance, and smart home living applications to drive our design.

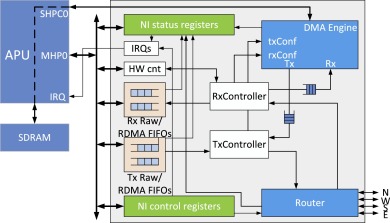
The OmpSs programming model supports the execution of heterogeneous tasks written in OpenCL, CUDA, or a high-level C or C++ language that can be converted to the machine language used in GPUs or converted to the bitstream to program FPGAs. Also, the runtime supports the communications within a cluster of distributed memory machines. OmpSs can target tasks to the different nodes of the cluster.



**AXIOM platform for next-generation cyber physical systems**

The AXIOM platform is designed around the Xilinx Zynq SoC (System on Chip) that features a multi-core ARM processor tightly coupled with FPGA fabric. AXIOM is designed to be modular at the next level, allowing the formation of more efficient processing systems through low cost, but scalable high-speed interconnect. The latter will utilize the integrated gigabit-rate transceivers with relatively low-cost USB-C connectors to interconnect multiple boards.

Such connectivity will allow to build (or upgrade at a later moment) flexible and low-cost systems by cascading more AXIOM boards, without the need of costly connectors and cables. AXIOM boards will feature two or four bi-directional links, so that the nodes can be connected in many different ways, such as ring and 2D-mesh/torus.



**AXIOM Network Interface Architecture**

The AXIOM network interface architecture has remote direct memory access (RDMA) and remote write operations as basic communication primitives visible at the application level.